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*Grace Alcea*  
Grace Alcea

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Amdt

Onnamillan  
7/12/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: July 7, 2003

CORREALE

Serial No. 10/065,226

Group Art Unit: 2816

Filed: September 26, 2002

Examiner: Wells, K.

For: METHOD AND CIRCUIT FOR OPTIMIZING POWER  
CONSUMPTION IN A FLIP-FLOP

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

In response to the Office Action dated April 7, 2003, please enter the following  
amendment and remarks in the present application:

RECEIVED  
JUL 11 2003  
TECHNOLOGY CENTER  
2800

07/10/2003 NMOHAMM1 00000072 500563 10065226

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